

Product Specification

产品规格书

LJ0707121Y

7" 1024(H)*3(RGB)*600(V) TFT LCD MODULE

May 23, 2019

Customer: _____

Customer Approval and Feedback

LONGCHI Signature:

Prepared by	Checked by	Approved by

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1. GENERAL DESCRIPTION

1.1 DESCRIPTION

LJ0707121Y is a transmissive type color active matrix TFT (Thin Film Transistor) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. This model is composed of a TFT-LCD module (TFT-LCD panel, driver IC and FPC), a back-light unit and. The resolution of 7.0" contains 1024RGBX600 pixels and can display up to 16.7M colors.

1.2 GENERAL INFORMATION

Items	Specification	Unit	Note
Display mode	TFT Transmissive, Positive, black, IPS	-	-
Drive element	a-Si TFT active matrix	-	-
LCM outline size	164.7(H) x 99.9(V) x 3.5(T)	mm	Note (1)(2)
Active area	154.08H)X85.92(V)	mm	-
Number of pixels	1024*3RGB(H)X600(V)	pixels	-
Pixel arrangement	RGB stripe	-	-
Pixel size	0.1506(W) x 0.1432 (H)	mm	-
Display color	16.7M	color	-
Viewing direction	ALL CLOCK	-	-
Controller / Driver	-	-	-
Data interface	24 BIT RGB Interface	-	
Backlight	18 White LEDs	-	
Weight	TBD	g	

Notes:

- (1) Touch panel and back-light unit are included.
- (2) FPC no included. (Refer to the module outline dimension for further information). Please see module specification drawing in Page14 for more details.

2. ABSOLUTE MAXIMUM RATING

(Ta=25±2°C, Vss=GND=0V)

Characteristics	Symbol	Min.	Max.	Unit	Notes
Power Supply Voltage 1	VDD1	-	-	V	
Power Supply Voltage 2	VDD2	-	-	V	
Power Supply Voltage 3	VDD3	-	-	V	
Power Supply Voltage 4	HS_VCC	-	-	V	
Power Supply Voltage 5	VSP	-	-	V	
Power Supply Voltage 6	VSN	-	-	V	
TFT Gate On voltage	VGH	12	+19	V	
TFT Gate Off voltage	VGL	-5.0	-10	V	
Logic Signal Input Voltage	V _{IN}	-0.3	VDD1+0.3	V	
HS Input Voltage	V _{IN}	-0.3	+2.0	V	
Backlight Forward Current	I _F	-	20	mA	
Operating Temperature	T _{OPR}	-10	+60	°C	(1), (3)
Storage Temperature	T _{STG}	-20	+70	°C	(2), (3)
Humidity	RH	-	90	%	Max. 60 °C

Notes:

- (1) In case of below 0°C, the response time of liquid crystal (LC) becomes slower and the color of panel becomes darker than normal one. Level of retardation depends on temperature, because of the LC characteristics.
- (2) If product is exposed to high temperatures for extended time, there is a possibility of the polarizer film damage which could degrade the optical characteristics.
- (3) Permanent damage to the device may occur if maximum values are exceeded or reverse voltage is loaded.
Functional operation should be restricted to the conditions described under normal operating conditions.

3. ELECTRICAL CHARACTERISTICS

3.1 LCM DC CHARACTERISTICS

(Ta=25±2°C)

Characteristics	Symbol	Min.	Typ.	Max.	Unit	Note
Power Supply Voltage 1	VDD	3	3.3	3.6	V	
Power Supply Voltage 2	AVDD	10	9.6	10.6	V	
Common Power Supply Voltage	VCOM	3.2	3.15	3.5	V	
Gate Driver High voltage	VGH	17	18	19	V	
Gate Driver Low voltage	VGL	-9	-6	-5	V	
Current Consumption	I _{DD}	-	TBD	-	mA	Normal mode
	I _{DD-SLEEP}		TBD		uA	Sleep mode
Input voltage "L" Level	V _{IL}	GND	-	0.3VDD	V	VDD1=1.65~3.6
Input voltage "H" Level	V _{IH}	0.7VDD	-	VDD	V	
Output voltage "L" Level	V _{oL}	-	-	GND+0.4	V	I _{oL} =1mA
Output voltage "H" Level	V _{oH}	VDD-0.4	-	-	V	I _{oH} =-1mA

3.2 BACK-LIGHT UNIT CHARACTERISTICS

The back-light system is an edge-lighting type with 7 white LEDs. The characteristics of the back-light are shown in the following tables.

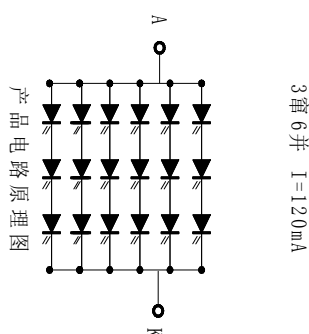
(Ta=25±2°C)

Characteristics	Symbol	Condition	Min.	Type	Max.	Unit	Notes
Forward Voltage	V _f	I _L =20mA	8.7	9.6	10.5	V	-
Forward current	I _L		--	120	-	mA	-
Luminance	L _v	I _L =20mA	TBD	TBD	--	cd/m ²	-
LED life time	-	I _L =20mA	10,000	20,000	--	Hr	Note 1

Note:

- (1) The "LED life time" is defined as the module brightness decrease to 50% of original brightness at I_L=20mA. The LED life time could be decreased if operating I_L is larger than 20mA.

Backlight circuit diagram shown in below:



4. OPTICAL CHARACTERISTICS

The following items are measured under stable conditions. The optical characteristics should be measured in a dark room.

Measuring equipment: BM-5AS, BM-7, EZ-Contrast.

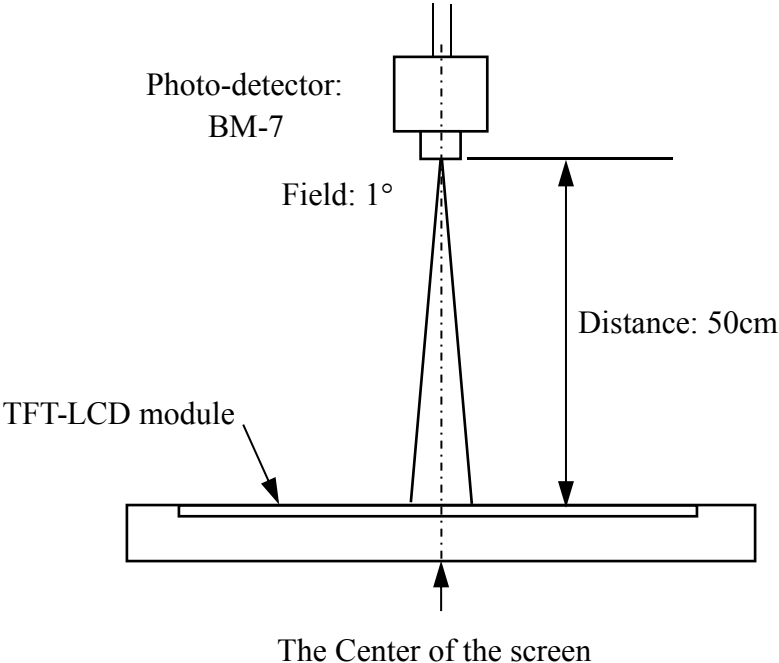
(Ta=25±2°C)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit	Note	
Contrast Ratio (Center point)	C/R	-	600	800	-	-	BM-7 Note(2)	
Luminance of white (Center point)	L _w	B/L on	220	240	260	cd/m ²	BM-7	
Luminance uniformity	U _w	θ = 0. Normal viewing angle B/L On Note(1)	80	-	-	%	BM-7 Note(3)	
Response Time	Tr + Tf		-	30	40	ms	BM-5AS Note(4)	
Color Chromaticity (CIE 1931)	White		W _x	-0.05	0.297	+0.05	-	BM-7 Note(5)
			W _y	-0.05	0.311	+0.05		
	Red		R _x	-0.05	0.555	+0.05		
			R _y	-0.05	0.324	+0.05		
	Green		G _x	-0.05	0.354	+0.05		
			G _y	-0.05	0.601	+0.05		
	Blue		B _x	-0.05	0.146	+0.05		
			B _y	-0.05	0.072	+0.05		
Viewing Angle	Hor.	θ _L	-	85	-	Deg	EZ Contrast Note(6)	
		θ _R	-	85	-			
	Ver.	θ _u	-	85	-			
		θ _D	-	85	-			
Optima View Direction		ALL					Note(7)	

* This condition will be changed by the evaluation circumstance. If product is exposed to high temperatures for extended time, there is a possibility of the polarizer film damage which could degrade the optical characteristics.

Notes:

- (1) Test Equipment Setup: After stabilizing and leaving the panel alone at a given temperature for 30min, the measurement should be executed. Measurement should be executed in a stable, windless, and dark room 30min after lighting the back-light. This should be measured in the center of screen.

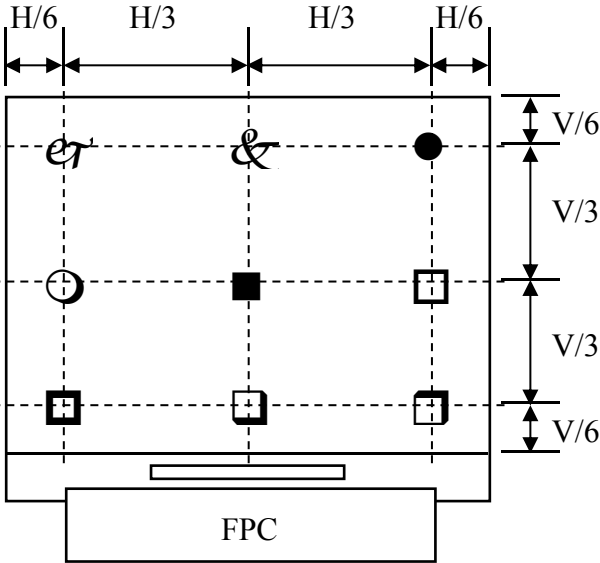


(2) Definition of Contrast Ratio (CR):

$$\text{Contrast Ratio (CR)} = \frac{\text{Luminance measured when LCD on the "white" state}}{\text{Luminance measured when LCD on the "black" state}}$$

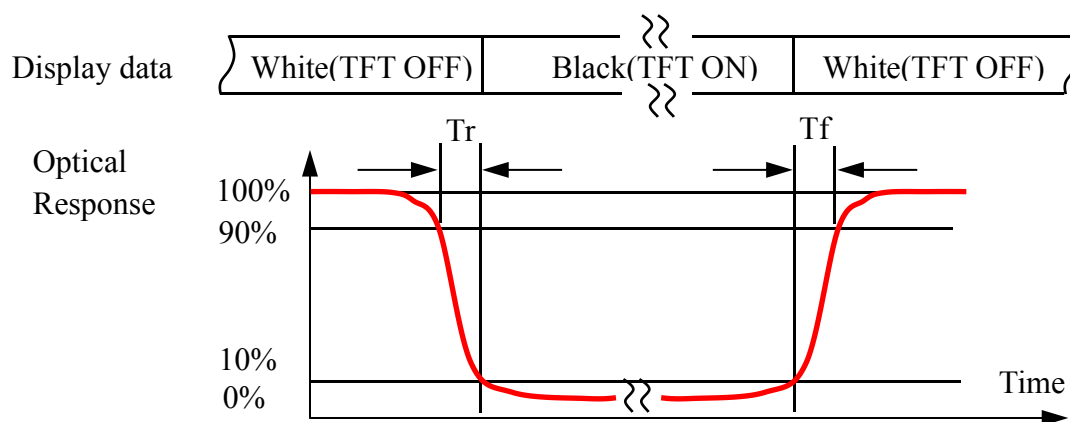
(3) Definition of Luminance Uniformity: Active area is divided into 9 measuring areas (Shown in below), every measuring point is placed at the center of each measuring area.

$$\text{Luminance Uniformity} = \frac{\text{Min Luminance of white among 9-points}}{\text{Max Luminance of white among 9-points}} \times 100\%$$

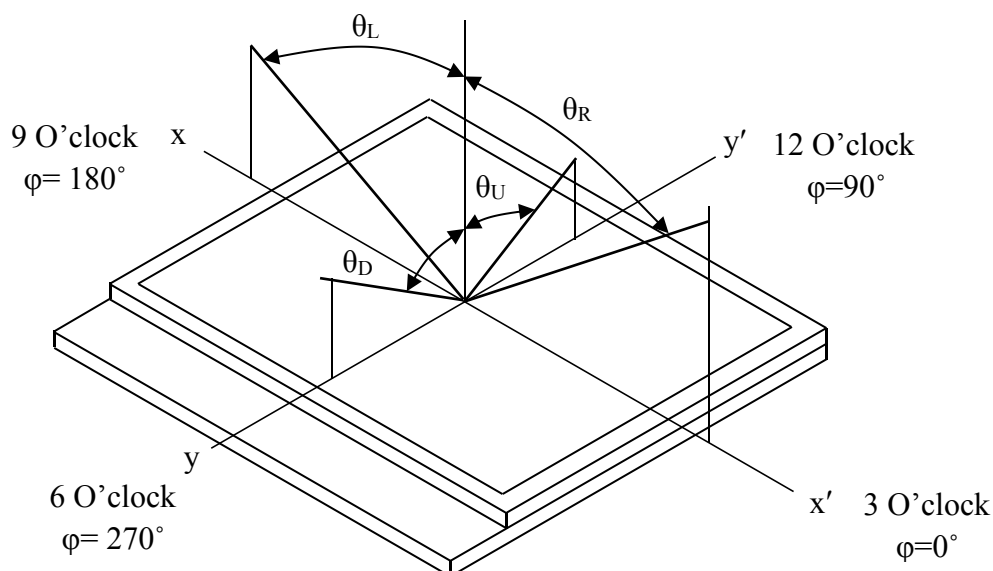


The spot locations for luminance measurement

- (4) Definition of Response time: Sum of T_r and T_f .

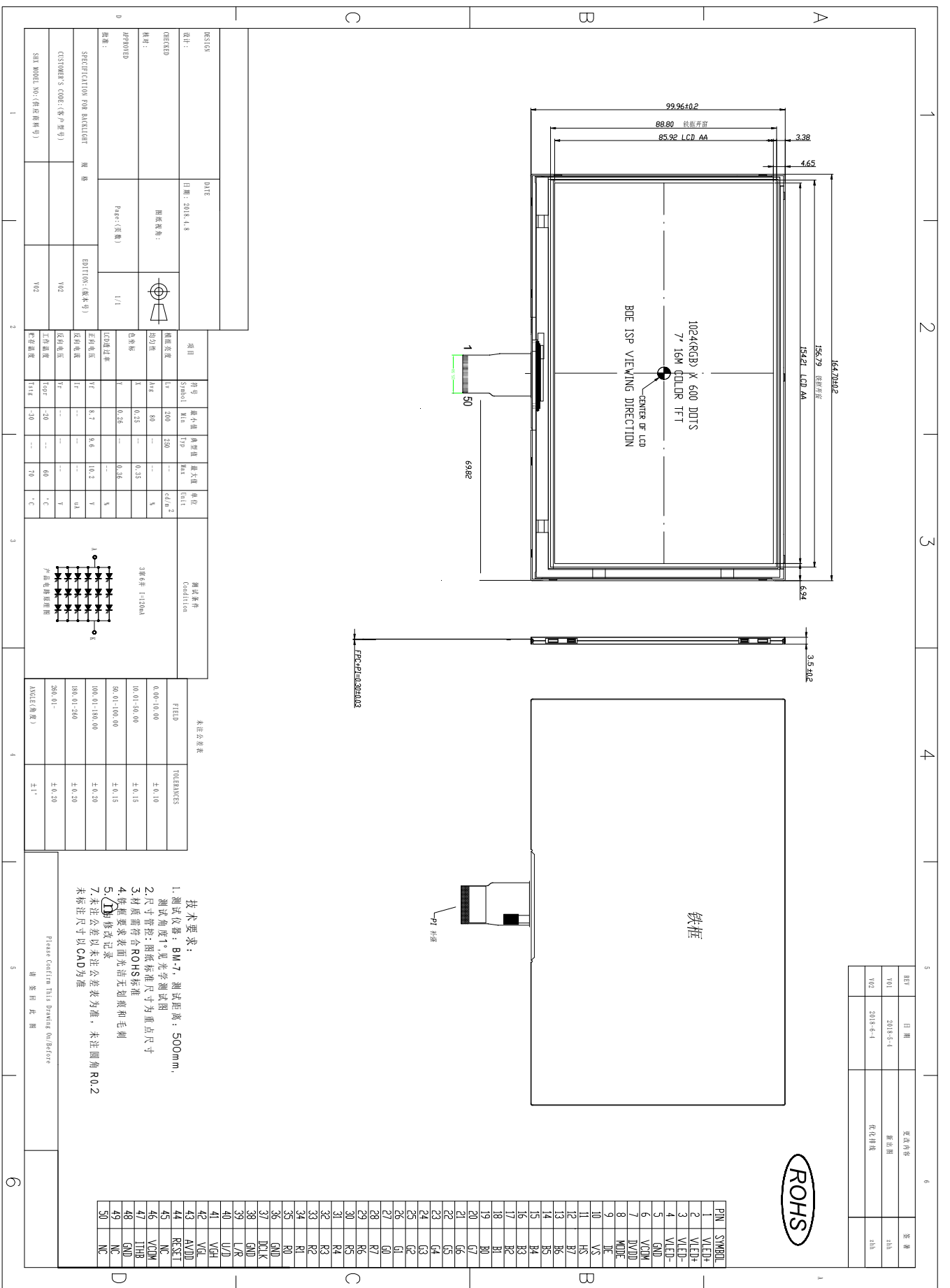


- (5) Definition of Viewing Angle: The viewing angle range that the $CR \geq 10$.



- (6) Definition of Color Chromaticity (CIE 1931)
Color coordinate of white & red, green, blue at center point.
- (7) The different Rubbing Direction will cause the different optima view direction.

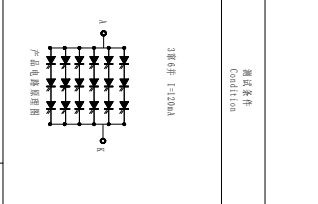
5. MODULE OUTLINE DIMENSION



料号	日期	更改内容	签署
V01	2018-5-4	量出图	zhh
V02	2018-5-4	优化图线	zhh



DESIGN	DATE	项目	序号	最小值	典型值	最大值	单位
设计:	日期: 2018.4.8	规格亮度	Symbol	Min	Typ	Max	Unit
CHECKED	审核员:	均匀性	L17	200	---	---	cd/m ²
材料:		色坐标	X	0.25	---	0.35	λ
APPROVED	批准:	对比度	T	0.26	---	0.36	λ
SPECIFICATION FOR BACKLIGHT	规格	工作电压	V1	8.7	9.6	10.2	V
CUSTOMER'S CODE: (客户型号)		额定电压	V1	---	---	---	NA
SER. MODEL NO.: (供应商型号)		工作温度	T097	-20	---	40	°C
		贮存温度	T14	-30	---	70	°C



FIELD	TOLERANCES
0.00-10.00	±0.10
10.01-50.00	±0.15
50.01-100.00	±0.15
100.01-180.00	±0.20
180.01-250	±0.20
250.01-	±0.30

技术要求:

- 测试仪器: BM-7, 测试距离: 500mm.
- 测试角度: 1° 见光学测试图
- 尺寸管控: 图样标注尺寸为重点尺寸
- 材质需符合 ROHS 标准
- 电镀要求表面光滑无划痕和毛刺
- 修改记录
- 未注公差以未注公差表为准, 未注圆角 R0.2
- 未标注尺寸以 CAD 为准

Please Confirm This Drawing On/Before
建 察 回 此 图

PN	SYMBOL
1	VLED+
2	VLED-
3	VLED-
4	VLED-
5	AND
6	VCOM
7	DVIDD
8	MUNE
9	DE
10	V/S
11	V/S
12	NS
13	B7
14	B5
15	B4
16	B3
17	B2
18	B1
19	R0
20	G7
21	G6
22	G5
23	G4
24	G3
25	G2
26	G1
27	G0
28	B7
29	B6
30	B5
31	B4
32	B3
33	B2
34	B1
35	R0
36	GND
37	DCLK
38	GND
39	L/R
40	U/D
41	VH
42	AVD
43	AVDD
44	RSET
45	NC
46	VCOM
47	THD
48	AND
49	NC
50	NC

7.Pin Assignment

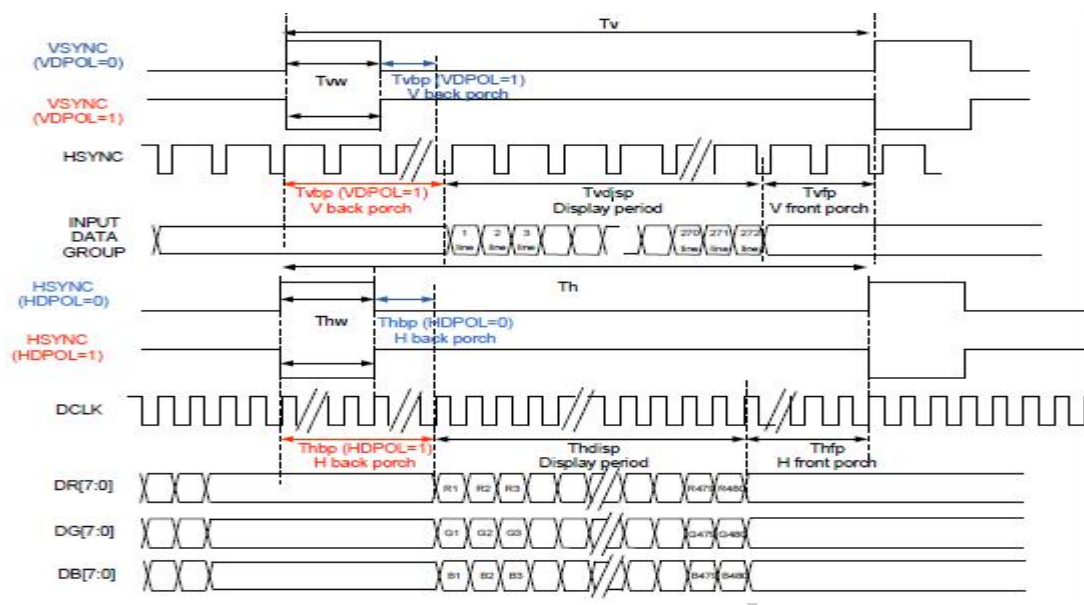
Pin No.	Symbol	I/O	Description	Note
1~2	LED+	P	LED Anode	
3~4	LED-	P	LED Cathode	
5	GND	P	Ground	
6	VCOM	P	Common Power Supply	
7	DVDD	P	Power supply	
8	MODE	I	DE/SYNC mode select,H:DE mode; L:SYNC mode	
9	DE	I	Data enable signal	
10	VS	I	Vertical sync input.Negative polarity.	
11	HS	I	Horizontal sync input.Negative polarity.	
12~19	B7~B0	I/O	Blue Data input	
20~27	G7~G0	I/O	Green Data input	
28~35	R7~R0	I/O	Red Data input	
36	GND	P	Ground	
37	DCLK	I	Clock input	
38	GND	P	Ground	
39	L/R	I	Left or Right Display Control	
40	U/D	I	Up and Down Display Control	
41	VGH	P	Positive Power for TFT	
42	VGL	P	Negative Power for TFT	
43	AVDD	P	Analog Power	
44	RESET	I	Global reset pin.	
45	NC	-	NC	
46	VCOM	P	Common Power Supply	
47	ITHB	I	Dithering function enable control.	
48	GND	P	Ground	
49	NC	-	NC	
50	NC	-	NC	

6. REFERENCE APPLICATION CIRCUIT

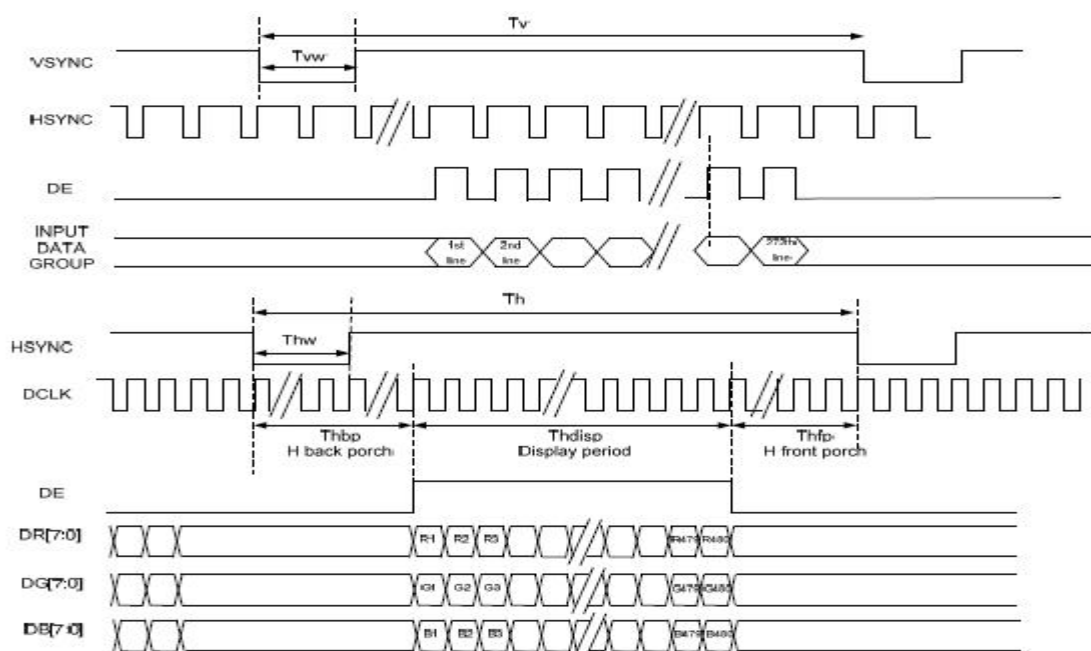
Please consult our technical department for detail information.

7. TIMINGS FOR 24-bit RGB Interface

8.1 SYNC Mode Timing Diagram



8.2 SYNC-DE Mode Timing Diagram



8. RELIABILITY TEST CONDITIONS

No.	Test Item	Test Condition	Notes
1	High Temperature Storage	+65°C / 120H	Inspection after 2~4h storage at room temperature, the sample shall be free from defects: 1. Air bubble in the LCD; 2. Seal leak; 3. Non-display; 4. Missing segments; 5. Glass crack; 6. The surface shall be free from damage. 7. The electrical characteristics requirements shall be satisfied.
2	Low Temperature Storage	-20°C / 120H	
3	High Temperature Operating	+60°C / 120H	
4	Low Temperature Operating	-10°C / 120H	
5	Temperature Cycle	0±2°CΔ25°CΔ+50±2°C x 10cycles (30min) (5min) (30min)	
6	High Temperature /Humidity storage	50+5°C x 90%RH / 120H	
7	Vibration Test	Frequency: 10Hz~55Hz~10Hz Amplitude:1.5mm, 2 hours for each direction of X, Y, Z	
8	Packing Drop Test	Drop to the ground from 1m height, 1 corner, 3 edges, 6 surfaces.	
9	ESD test	Voltage:±8KV R: 330Ω C: 150pF Air discharge, 10time	

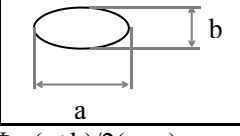
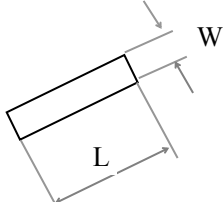
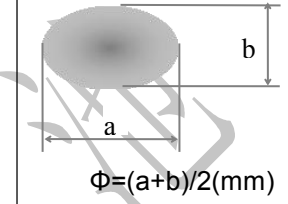
Remarks:


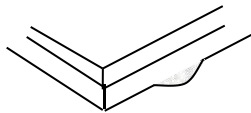
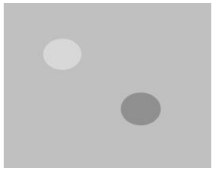
- (1) The test samples should be applied to only one test item.
- (2) Sample size for each test item is 5~10pcs.
- (3) For High Temperature/Humidity storage test, pure water (resistance>10MΩ) should be used.
- (4) In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judge as a good part.
- (5) Failure judgment criterion: basic specification, electrical characteristic, mechanical characteristic, optical characteristic.

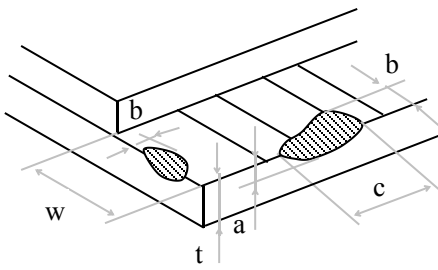
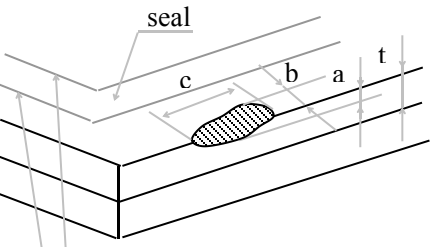
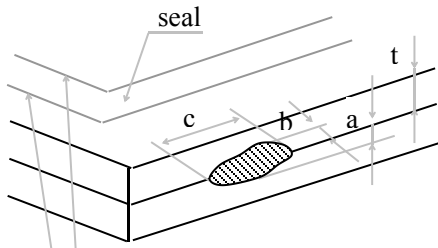
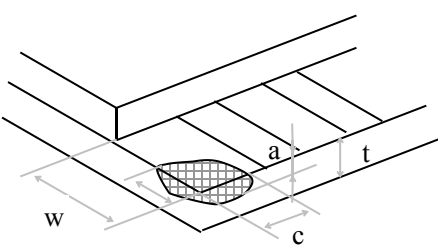
9. PACKING SPECIFICATION

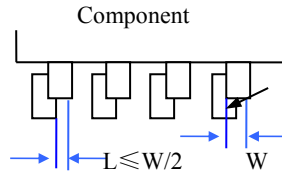
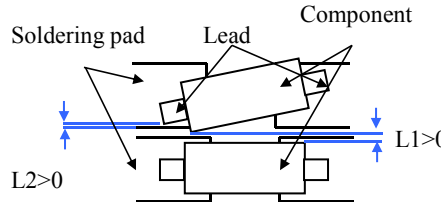
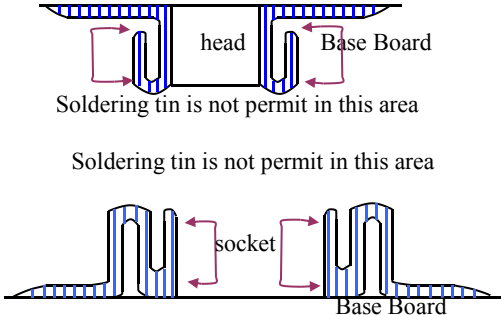
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10. INSPECTION CRITERION

Inspection item		Judgement standard				
		Category		Acceptable number		
				A zone	B zone	
1	Black spot, White spot, Bright Spot, Pinhole Foreign Particle, Bubble and Particle Between polarizer and glass, scratch on polarizer		A B C D	$\Phi \leq 0.15$ $0.15 < \Phi \leq 0.20$ $0.20 < \Phi \leq 0.30$ $0.30 < \Phi$	Ignored 2 1 0	Ignored
	Pixel point defect	Bright spot		$0.15 < \Phi \leq 0.20$	$N \leq 0$	Ignored
		Dark spot/ Black spot		$0.15 < \Phi \leq 0.20$	$N \leq 2$	
		Attached to the two pixels are bright spots		$0.15 < \Phi \leq 0.20$	$N \leq 0$	
		Even a two pixel is dark		$0.15 < \Phi \leq 0.20$	$N \leq 0$	
		Pixel total number		$0.15 < \Phi \leq 0.20$	$N \leq 2$	
Note1: the spot defect caused by foreign matter is judged according to the defect of the foreign body. Note 2: when the light is not wired to show the type of defects.						
2	Black line, White line, Bubble and Particle Between Polarizer and glass, Scratch on polarizer		A B C D	$W \leq 0.10$ $0.01 < W \leq 0.03 \quad L \leq 3.0$ $0.03 < W \leq 0.05 \quad L \leq 3.0$ $0.05 < W$	Ignored 2 1 0	Ignored
	W:Width, L:Length(mm)		Total defective point(B,C)		2	
3	Contrast variation		A B C D	$\Phi \leq 0.2$ $0.2 < \Phi \leq 0.3$ $0.3 < \Phi \leq 0.4$ $0.4 < \Phi$	Ignored 2 1 0	Ignored
	$\Phi=(a+b)/2(\text{mm})$		Total defective point(B,C)		3	
4	Bubble inside cell		any size		none	none
5	Polarizer defect (if Polarizer is used)	Scratch and damage on polarizer, particle on polarizer or between polarizer and glass.	Refer to item 1 and item 2.			
		Bubble, dent and convex	A B C	$\Phi \leq 0.3$ $0.3 < \Phi \leq 0.7$ $0.7 < \Phi$	Ignored 2 0	Ignored
		Total defective point(B,C)		2		

Inspection item		Judgement standard		
		Category	Acceptable number	
			A zone	B zone
6	Surplus glass	①Stage surplus glass 	$b \leq 0.3\text{mm}$	
		②Surrounding surplus glass 	Should not influence outline dimension and assembling.	
7	MURA	①MURA	<p>Naked eye examination: red, green, blue screen does not allow the appearance, black screen requires visual is not obvious, the specific reference limit samples.</p> <p>Note: the principle of closing the sample is to be installed on the whole machine and the end user will not find it in the normal usage scenario.</p> <p>Inspection basis: 6%ND (MURA mainly in the black screen and indoor light is relatively dark will be found, it is recommended to turn off the indoor lighting inspection.)</p>	
		②Point Black / White / point(MURA)	<p>1, under the black / gray screen check: $D \leq 0.10\text{mm}$ Ignored; $0.10\text{mm} < D \leq 0.3\text{mm}$, $N \leq 2$; $D > 0.3\text{mm}$: Unqualified.</p> <p>2, switch to the red, green, blue in which any one of the screen appears black or white or point to point white or point of failure.</p> 	

Inspection item		Judgment standard		
		Category(application: B zone)		
8	Glass defect crack	①The front of lead terminals	A	If $a \leq t$ and $b \leq 1.0$, c is not limited
			B	$a \leq t$, $1 \leq b \leq 2\text{mm}$, $c \leq 3\text{mm}$
			C	If glass crack cover alignment mark, $b \leq 0.5\text{mm}$.
			D	Crack at two sides of lead terminals should not cover patterns and alignment mark
	②Surrounding crack—non-contact side	 <p><u>Inner border line of the seal</u> <u>Outer border line of the seal</u></p>		
③ Surrounding crack— contact side	 <p><u>Inner border line of the seal</u> <u>Outer border line of the seal</u></p>			
	④Corner	A	$a \leq t$, $b \leq 3.0$, $c \leq 3.0$ *Glass crack should not cover patterns used for	
				

		Inspection item	Judgement standard
9	FPC defect	<p>Component soldering: No cold soldering, short/open circuit, burr, tin ball.</p> <p>The flat encapsulation component position deviation must be less than 1/2 width of the pin (Pic.1);</p> <p>The sheet component deviation: pin deviates from the pad and contact with the near components is not permitted (Pic.2)</p>	 <p>Component</p> <p>$L \leq W/2$</p> <p>W</p>
		<p>lead defect:</p> <p>The lead lack must be less than 1/2 of its width;</p> <p>The lead burr must be less than 1/2 of the seam;</p> <p>Impurities connect with the near leads is not permitted</p>	 <p>Soldering pad</p> <p>Lead</p> <p>Component</p> <p>$L2 > 0$</p> <p>$L1 > 0$</p>
		<p>Connector soldering:</p> <p>Soldering tin is at contact position of the plug and socket is not permitted</p> <p>No foundation is scald</p> <p>Serious cave distortion on plug and socket contact pin is not permitted</p>	 <p>head</p> <p>Base Board</p> <p>Soldering tin is not permit in this area</p> <p>Soldering tin is not permit in this area</p> <p>socket</p> <p>Base Board</p>

11. GENERAL PRECAUTIONS

1.1 HANDING

- (1) When the module is assembled, it should be attached to the system firmly. Be careful not to twist and bent the module.
- (2) Refrain from strong mechanical shock and / or any force to the module. In addition to damage, this may cause improper operation or damage to the module and back-light unit.
- (3) Note that display modules are very fragile and could be easily damaged. Do not press or scratch the surface harder than a HB pencil lead.
- (4) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, straining and discoloration may occur.
- (5) If the display module surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, should be wiped by moisten cloth with isopropyl alcohol or ethyl alcohol solvents, DO NOT with water, ketone type materials (e.g. acetone), aromatic, toluene, ethyl acid or methyl chloride, and so on.
- (6) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, legs or clothes, it must be washed away thoroughly with soap.
- (7) Use finger-stalls with sort gloves in order to keep display clean during the incoming inspection and assembly process.
- (8) Protection film for polarizer on the module shall be slowly peeled off just before use so that the electrostatic charge can be minimized.
- (9) Do not touch directly conductive parts such as the CMOS LSI pad and the interface terminals with bare hands, therefore operations should be grounded whenever he/she comes into contact with the modules.
- (10) Do not exceed the absolute maximum rating value. (The supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on), otherwise the module may be damaged.

1.2 SOLDERING

- (1) Use soldering irons with proper grounding and no leakage.
- (2) For No RoHS Product: soldering temperature is 290~350°C, soldering time is 3~5s; for RoHS Product: soldering temperature is 340~370°C, soldering time is 3~5s.
- (3) If soldering flux is used, be sure to remove any remaining flux after soldering (This does not apply in the case of a non-halogen type of flux).

1.3 STORAGE

- (1) DO NOT leave the module in high temperature and high humidity for a long times, keep the temperature from 0°C to 35°C and relative humidity of less than 60%.
- (2) It is highly recommended to store the module in a dark place. The Liquid crystal is deteriorated by ultraviolet, DO NOT leave it in direct sunlight and strong ultraviolet ray for many hours.
- (3) The polarizer surface should not come in contact with any other objects.